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Durbano, J.P.; Ortiz, F.E.; Humphrey, J.R.; Mirotznik, M.S.; Prather, D.W.;

Antennas and Wireless Propagation Letters Volume 2, Issue 1, 2003 Page(s):54 - 57

Digital Object Identifier 10.1109/LAWP.2003.812245

AbstractPlus | Full Text: PDF(176 KB) | IEEE JNL

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2. Finite-difference time-domain method in custom hardware?

Schneider, R.N.; Okoniewski, M.M.; Turner, L.E.;

Microwave and Wireless Components Letters, IEEE [see also IEEE Microwave Wave Letters]

Volume 12, Issue 12, Dec. 2002 Page(s):488 - 490 Digital Object Identifier 10.1109/LMWC.2002.805948

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3. A prototype FPGA finite-difference time-domain engine for electromagnet

Culley, R.; Desai, A.; Gandhi, S.; Shugaung Wu; Tomko, K.; Circuits and Systems, 2005. 48th Midwest Symposium on

7-10 Aug. 2005 Page(s):663 - 666 Vol. 1

Digital Object Identifier 10.1109/MWSCAS.2005.1594188

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4. FPGA implementation of FDTD algorithm

Suzuki, H.; Takagi, Y.; Yamaguchi, R.; Uebayashi, S.;

Microwave Conference Proceedings, 2005. APMC 2005. Asia-Pacific Conferer

Volume 3, 4-7 Dec. 2005 Page(s):4 pp.

Digital Object Identifier 10.1109/APMC.2005.1606686

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5. FPGA finite difference time domain solver for thermal simulation

Pardo, E.; Lopez, R.; Cabello, D.; Balsi, M.;

Field Programmable Logic and Applications, 2005. International Conference or

24-26 Aug. 2005 Page(s):721 - 722

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Ryan N. Schneider, Laurence E. Turner, Michal M. Okoniewski

February 2002 Proceedings of the 2002 ACM/SIGDA tenth international symposium on Field-programmable gate arrays FPGA '02

Publisher: ACM Press

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January 1980 ACM SIGACT News, Volume 12 Issue 1

Publisher: ACM Press

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March 2001 Proceedings of the conference on Design, automation and test in Europe **DATE '01** 

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